

Notice of References Cited	Application/Control No. 10/666,354	Applicant(s)/Patent Under Reexamination CHANG ET AL.	
	Examiner Maki A. Angadi	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,147,009 A	11-2000	Grill et al.	438/780
*	B	US-6,936,533 B2	08-2005	Kim et al.	438/623
	C	US-			
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	S. Wolf, Silicon Processing for the VLSI Era, Vol.1, page 441, Lattice Press, (1986)
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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